

MARKED UP VERSION OF AMENDMENTS TO THE SPECIFICATION

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RELATED APPLICATIONS

This application is a divisional application of U.S. Application No. 09/003,670 filed on January 7, 1998.

MARKED UP VERSION OF AMENDMENTS TO THE CLAIMS

Claim 18 was amended as follows:

18. (Amended) An apparatus for surface treating a semiconductor wafer comprising:
- a surface treatment chamber; and
- a source of radiation illuminating a semiconductor wafer disposed inside the chamber under ambient pressure and atmosphere with a radiation having a wavelength in a near infrared range sufficient to create a plurality of electron-hole pairs near a surface of the wafer and to desorb any contaminant adsorbed on the surface of the wafer.

New claims 27-29 were added as follows:

27. A method for characterizing a semiconductor wafer comprising:
- restoring an inversion condition at a surface of the wafer by illuminating the surface of the wafer with light; and
- measuring an electrical characteristic of the wafer using the inversion condition as a reference point.
28. The method of claim 27 wherein the wafer comprises a p-type wafer.
29. The method of claim 27 wherein restoring an inversion condition comprises restoring a positive charge at the surface of the wafer.

CLEAN COPY OF ALL PENDING CLAIMS

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25. The apparatus of claim 18 further comprising a filter disposed between the radiation source and the wafer for filtering radiation having wavelength greater than about 4 microns.

26. The apparatus of claim 18 further comprising a first filter disposed between the radiation source and the wafer, a second filter disposed adjacent the first filter, and an air passageway disposed between the first filter and the second filter for cooling the filters, wherein the first filter and the second filter prevents radiation having wavelengths greater than about 4 microns from reaching the wafer.

27. A method for characterizing a semiconductor wafer comprising:
restoring an inversion condition at a surface of the wafer by illuminating the surface of the wafer with light; and
measuring an electrical characteristic of the wafer using the inversion condition as a reference point.

28. The method of claim 27 wherein the wafer comprises a p-type wafer.

29. The method of claim 27 wherein restoring an inversion condition comprises restoring a positive charge at the surface of the wafer.